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		Application Number	10/600,563		
		Filing Date	6/19/2003		
		First Named Inventor	Gunn III, et al.		
		Art Unit	2826		
		Examiner Name	Dickey, Thomas L		
Sheet	1	of	1	Attorney Docket Number	LUX-P003

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TD/	1	ROBERT F. PIERRET, "Semiconductor Device Fundamentals," Prentice Hall, 2nd Revised Edition, 1995	
/TD/	2	ANDREW MARSHAL and SREEDHAR NATARAJAN, " SOI Design: Analog, Memory and Digital Techniques," Springer, 2001	
/TD/	3	Sleight, J. W., et al. "A High Performance 0.13 μ m SOI CMOS Technology with a 70 nm Silicon Film and with a Second Generation Low-k Cu BEOL," Electron Device Meeting, 2001	
/TD/		IEDM Technical Digest, page 11.3.1-11.3 (2001)	

Examiner Signature	/Thomas Dickey/	Date Considered	07/03/2008
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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